MICHAEL ANDERSON

Embedded VLSI Design Engineer

- San Francisco, CA
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Experienced VLSI Design Engineer with a focus on embedded systems and hardware-software co-design. Over 9 years of diverse experience in the semiconductor industry, adept at developing solutions for complex design challenges. Proficient in various programming languages and hardware description languages, enabling effective collaboration with software teams. Strong problem-solving abilities and a commitment to delivering high-quality designs within project timelines.

WORK EXPERIENCE

Embedded VLSI Design Engineer | Embedded Solutions Inc. Jan 2022 – Present

- Designed and implemented embedded systems for automotive applications, improving efficiency by 30%.
- Collaborated with software engineers to develop hardware-software interfaces.
- Utilized SystemVerilog for RTL coding, ensuring high-quality design outputs.
- Conducted performance evaluations and optimizations on existing designs.
- Participated in project planning meetings, contributing to timeline and resource allocation.
- Provided training and mentorship to junior engineers, enhancing team skills.

VLSI Design Engineer | Circuit Innovations Ltd. Jul 2019 – Dec 2021

- Engaged in the design and verification of SoC for consumer electronics.
- Utilized advanced simulation tools to validate design performance and reliability.
- Participated in integration testing, ensuring proper functionality of designs.
- Collaborated with cross-functional teams to define project specifications.
- Maintained comprehensive documentation for design processes and outcomes.
- Supported project management activities, assisting in timeline adherence.

SKILLS

Embedded Systems | Hardware-Software Co-Design | SystemVerilog | Performance Optimization | Project Management | Team Leadership

EDUCATION

Master of Engineering in Electrical and Computer Engineering 2015 – 2019

University of Toronto

ACHIEVEMENTS

- Recognized for developing a system that enhanced automotive safety features.
- Improved design verification processes, reducing time-to-market by 15%.
- Contributed to a publication on embedded system design strategies at an international conference.

LANGUAGES

English | Spanish | French