MICHAEL ANDERSON

LEAD VLSI DESIGN ENGINEER

CONTACT

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San Francisco, CA

SKILLS

- Low-Power Design
- Digital Design
- DFM
- Cadence
- Verilog
- Simulation

LANGUAGES

- English
- Spanish
- French

EDUCATION

BACHELOR OF SCIENCE IN COMPUTER ENGINEERING, UNIVERSITY OF CALIFORNIA, BERKELEY

ACHIEVEMENTS

- Recognized for leading a project that won the 'Best Innovation' award at the annual tech expo.
- Improved design turnaround time by 20% through process optimization.
- Contributed to a publication on low-power design strategies in a prominent conference.

PROFILE

Dynamic VLSI Design Engineer with over 10 years of experience specializing in low-power design techniques and methodologies. Adept at working in fast-paced environments and managing multiple projects concurrently. Strong technical proficiency in digital design and verification processes, with a focus on optimizing energy efficiency. Solid understanding of semiconductor manufacturing processes and design for manufacturability (DFM).

EXPERIENCE

LEAD VLSI DESIGN ENGINEER

NextGen Semiconductors

2016 - Present

- Directed low-power VLSI design projects, achieving a 40% reduction in power consumption.
- Implemented advanced DFM techniques, enhancing yield rates during production.
- Collaborated with R&D teams to integrate new technologies into existing designs.
- Conducted extensive simulations using Cadence tools to validate design integrity.
- Provided technical leadership and guidance to a team of junior engineers.
- Authored design specifications and documentation for new product introductions.

VLSI DESIGN ENGINEER

Future Tech Corp.

2014 - 2016

- Designed and verified complex digital systems, meeting customer requirements.
- Utilized Verilog for RTL design, achieving high levels of synthesis efficiency.
- Participated in design reviews and provided feedback to improve design quality.
- Engaged in power and timing analysis to ensure compliance with specifications.
- Developed test benches for thorough verification of designs.
- Created and maintained project documentation for knowledge transfer.