MA

Michael
ANDERSON

LEAD SOC DESIGN ENGINEER

Experienced SoC Design Engineer with a focus on high-speed networking applications and over 8 years in the telecommunications industry. Proven track record of developing SoC solutions that meet stringent performance and reliability requirements. Adept at working with multi-disciplinary teams to drive product development from concept to market. Strong proficiency in digital design methodologies and FPGA prototyping.

CONTACT

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- San Francisco, CA

SKILLS

- high-speed design
- networking
- FPGA prototyping
- digital design
- project leadership
- collaboration

LANGUAGES

- English
- Spanish
- French

EDUCATION

MASTER OF SCIENCE IN TELECOMMUNICATIONS ENGINEERING, GEORGIA INSTITUTE OF TECHNOLOGY

ACHIEVEMENTS

- Recognized with 'Outstanding Engineer' award for exceptional contributions to 5G projects.
- Successfully led a project that reduced design cycle time by 30% through process improvements.
- Contributed to a white paper on the future of SoC design in telecommunications.

WORK EXPERIENCE

LEAD SOC DESIGN ENGINEER
Telecom Innovations Corp.
2020 - 2025

- Designed high-speed SoCs for 5G networking equipment, achieving throughput improvements of 40%.
- Led a team of engineers through the entire design cycle from architecture to production.
- Utilized Xilinx and Intel FPGAs for prototyping and validation, reducing development time by 25%.
- Collaborated with marketing teams to define product features based on customer feedback.
- Conducted training sessions for junior engineers, fostering a culture of knowledge sharing.
- Maintained compliance with telecom standards, ensuring product reliability and safety.

SOC DESIGN ENGINEER
CommTech Solutions
2015 - 2020

- Developed SoCs for broadband applications, optimizing data throughput and reducing latency.
- Engaged in extensive simulation and testing to validate design integrity and performance.
- Worked closely with system architects to define requirements and validate design decisions.
- Implemented design changes based on performance testing, enhancing system reliability.
- Participated in cross-functional teams to address design challenges and optimize outcomes.
- Documented design changes and maintained comprehensive records for compliance audits.