



MICHAEL ANDERSON

ASIC DESIGN ENGINEER

CONTACT

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- San Francisco, CA

SKILLS

- Mixed-signal design
- EDA tools
- MATLAB
- signal integrity
- SystemVerilog
- collaboration
- testing protocols

LANGUAGES

- English
- Spanish
- French

EDUCATION

BACHELOR OF SCIENCE IN ELECTRICAL ENGINEERING, UNIVERSITY OF CALIFORNIA, BERKELEY

ACHIEVEMENTS

- Recognized as 'Employee of the Month' for exceptional contributions to project success.
- Reduced design iterations by 15% through effective simulation techniques.
- Successfully supported a major product launch that increased market share by 10%.

PROFILE

Results-driven ASIC Design Engineer with a robust background in digital and mixed-signal design, complemented by over 8 years of experience in the telecommunications sector. Known for innovative problem-solving skills and ability to work under tight deadlines, I have successfully led multiple projects from concept to production.

EXPERIENCE

ASIC DESIGN ENGINEER

Global Telecom Solutions

2016 - Present

- Designed and validated mixed-signal ASICs for advanced telecommunication applications, enhancing signal integrity.
- Utilized MATLAB and Cadence tools to model and simulate circuit performance, ensuring optimal functionality.
- Collaborated with cross-disciplinary teams to develop integrated solutions that improved product offerings.
- Implemented rigorous testing protocols, reducing post-production failures by 25%.
- Conducted design reviews and provided feedback to enhance project outcomes.
- Assisted in the transition of designs from R&D to mass production, ensuring manufacturability.

JUNIOR ASIC DESIGN ENGINEER

ElectroTech Solutions

2014 - 2016

- Supported senior engineers in the development of ASIC designs for consumer electronics, contributing to product launches.
- Utilized EDA tools for synthesis and timing closure during the design process.
- Performed functional verification using SystemVerilog to validate design specifications.
- Assisted in creating detailed documentation for design processes and results.
- Contributed to team meetings, providing insights that improved design strategies.
- Learned best practices in ASIC design and verification through hands-on experience and mentorship.